

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10595426	
	Filing Date			
	First Named Inventor	Harald Jacobsson		
	Art Unit			
	Examiner Name	Unassigned		
	Attorney Docket Number		0110-091	

U.S.PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/D.P./	1	6469584	B1	2002-10-22	Eker et al.	
/D.P./	2	5497127	A	1996-03-05	Sauer	

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/D.P./	1	20020175769	A1	2002-11-28	Wong et al.	

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/D.P./	1	2004004126	WO	A1	2004-01-08	Advance Micro Devices, Inc.		<input type="checkbox"/>

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/D.P./	1	PALERMO et al., "A Multi-Band Single-Loop PLL Frequency Synthesizer with Dynamically-Controlled Switched Tuning VCO", Proc. 43rd IEEE Midwest Symp. On Circuits and Systems, Lansing, MI, August 2000, pp 818-821	<input type="checkbox"/>
/D.P./	2	WILSON, W.B. et al., "A CMOS Self-Calibrating Frequency Synthesizer", IEEE Journal of Solid-State Circuits, Vol. 35, No. 10, October 2000, pp 1437-1444	<input type="checkbox"/>
/D.P./	3	LIN, Tsung-Hsien et al., "A 900-MHz 2.5-mA CMOS Frequency Synthesizer with an Automatic SC Tuning Loop", IEEE Journal of Solid-State Circuits, Vol. 36, No. 3, March 2001, pp 424-431	<input type="checkbox"/>
/D.P./	4	PCT International Search Report, mailed May 19, 2004, in connection with International Application No. PCT/SE2003/001641	<input type="checkbox"/>
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